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RS449, or ... The **interface** between the Data **Communications** Equipment (DCE), ...

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... to give sufficient detail to define the **interface** towards other parts of the
... an **FPGA**, which apart from decoding the SPECS bus **communication** provides ...

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... Each module has a port **interface**, which defines the inputs and outputs ...
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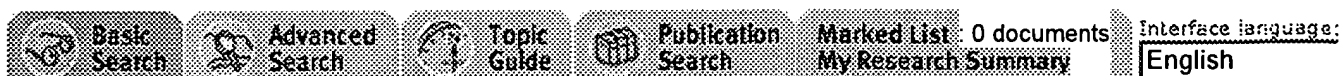
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